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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/019,696	06/20/2002	David Finn	70357	3879
23872	7590	12/04/2003	EXAMINER	
MCGLEW & TUTTLE, PC SCARBOROUGH STATION SCARBOROUGH, NY 10510			VU, QUANG D	
			ART UNIT	PAPER NUMBER
			2811	

DATE MAILED: 12/04/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

10/019,696

Applicant(s)

FINN ET AL.

Examiner

Quang D Vu

Art Unit

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 21 October 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-7 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. §§ 119 and 120**

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☒ All b) ☐ Some \* c) ☐ None of:  
1. ☒ Certified copies of the priority documents have been received.  
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).  
\* See the attached detailed Office action for a list of the certified copies not received.
- 13) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application) since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78.  
a) ☐ The translation of the foreign language provisional application has been received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121 since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) \_\_\_\_\_ 6) ☐ Other: \_\_\_\_\_

## **DETAILED ACTION**

### ***Election/Restrictions***

Applicant's election with traverse of group I, claims 1-7 in Paper No. 10 is acknowledged. The traversal is on the ground (s) that the restriction is not proper. This is not found persuasive because group II (claims 8-14) and group I (claims 1-7) are related as process of making and product made, respectively. Additionally, the device of group I (claims 1-7) invention could be made by a materially different process. For example, forming the chip and the contact metallizations of the chip on the temporary substrate and contacting the conductive connection strands with the carrier film to the contact metallizations of the chip, and then remove the temporary substrate, instead of applying conductive connection strands on the carrier film and contacting contact metallizations of the chip with the conductive connection strands.

The requirement is still deemed proper and is therefore made FINAL.

### ***Claim Rejections - 35 USC § 102***

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-4 and 6 are rejected under 35 U.S.C. 102(b) as being anticipated by US Patent No. 5,176,853 to Sarma et al.

Regarding claim 1, Sarma et al. (figure 9) teach a chip carrier for forming a chip module, the chip carrier comprising:

a substrate formed by a carrier film (409); and  
connection leads (509) arranged on the substrate, the connection leads (509) comprising strips and extend parallel over the substrate, the connection leads (509) comprising electrically conductive connection strands arranged on the substrate in a single plane and extending in a planar direction over the substrate surface and having a longitudinal expansion flush with the substrate surface.

Regarding claim 2, Sarma et al. inherently teach the carrier film (409) is provided with at least one additional conductive strand (98) on a side opposite the connection strands (509) to generate a capacitance, wherein the insulating carrier film (409) is arranged as an intermediated layer between the connection strands (509) on the one hand and the conductive strand (98) on the other.

Regarding claim 3, Sarma et al. teach the connection strands (509) are at least sectional provided with a connecting material coating for contacting with the contact metallizations (309) of a chip (209).

Regarding claim 4, Sarma et al. teach the connection strands (509) are at least sectional provided with a contact metallizations (309) for contacting with the contact metallizations of a chip (209).

Regarding claim 6, Sarma et al. (figure 9) teach a chip module, comprising:  
a chip carrier comprising a substrate formed by a carrier film (409) and connection leads (509) arranged on the substrate, the connection leads (509) comprising stripes and extend parallel

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over the substrate, the connection leads (509) comprising electrically conductive connection strands arranged on the substrate in a single plane and extending in a planar direction over the substrate surface and having a longitudinal expansion flush with the substrate surface; and

connecting surfaces with elevated contact metallizations (309), the contact metallizations (309) being in contact with a top side of the connection strands (509) facing away from the carrier film (409).

***Claim Rejections - 35 USC § 103***

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over US Patent No. 5,176,853 to Sarma et al. in view of US Patent No. 6,326,233 to Hashimoto.

Regarding claim 5, Sarma et al. differ from the claimed invention by not showing the connection strands are connected with the terminals of a coil unit. However, Hashimoto (figures 7-8) teaches the connection strands (20) are connected with the terminals of a coil unit. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to incorporate the teaching of Hashimoto into the device taught by Sarma et al. because it increase the productivity of the device. The combined device shows the connection strands are connected with the terminals of a coil unit.

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Regarding claim 7, Sarma et al. teach the connection strands (509) are in contact with the contact metallizations (309) of the chip (209). Sarma et al. differ from the claimed invention by not showing the connection strands are connected with the terminals of a coil unit. However, Hashimoto (figures 7-8) teaches the connection strands (20) are connected with the terminals of a coil unit. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to incorporate the teaching of Hashimoto into the device taught by Sarma et al. because it increase the productivity of the device. The combined device shows the connection strands are connected with the terminals of a coil unit.

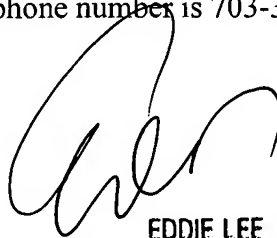
### *Conclusion*

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Quang D Vu whose telephone number is 703-305-3826. The examiner can normally be reached on Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Lee can be reached on 703-308-1690. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

qv  
November 28, 2003



EDDIE LEE  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2800